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FOR IMMEDIATE RELEASE

SYNTHESYS RESEARCH, INC. EXPANDS ITS HDTV OFFERING WITH HDEYE292 JITTER ANALYZER AT NAB2000

Las Vegas, Nevada, April 10, 2000 -- SyntheSys Research, Inc., of Menlo Park, California, has chosen the National Association of Broadcasters show as the launching point for two new products that support the digital television transition going on throughout the world today.

SyntheSys Research, Inc., announced today the addition of the HDEYE292 Jitter Analyzer to the award winning BitAlyzer®HDTV family of products. Eye Diagramming and Jitter Spectrum Analysis on the 1.485 Gbit/sec signal will allow users to rapidly isolate problems causing bit errors in digital communications. "Our customers have been pressing hard to get eye diagramming and jitter spectrum analysis added into the HDTV product family," says Bob Titus, Product Manager. "With this addition," continues Titus, "engineers will be able to quickly test their newly-designed HDTV facilities and eliminate unwanted problems that can be found in HDTV data streams."

The new instrument displays Eye diagrams and FFT Analysis of Jitter on 1.485 Gb/s SMPTE 292M studio HDTV signals. "Jitter testing on SDTV serial digital signals has been around for many years," says Titus. "our customers know how important jitter testing is from their experience with SDTV signals. HDTV signals are 5.5 times faster, so jitter testing these signals is even more important." The HDEYE292 Jitter Analyzer may be configured with HDTV signal format testing and test-pattern generation. This provides users with a single unit capable of stimulating an HDTV system, measuring physical-layer performance parameters including Jitter, and testing format protocol compliance with SMPTE standard 292M.

Additionally, SyntheSys Research, Inc., is introducing an integrated solution that measures bit error rates within standard definition SDI signals. This solution incorporates a 270 Mb/s serial digital video interface with the company's patented BitAlyzer® Error Location Analyzer to provide an end-to-end solution for bit error rate testing. For added I/O flexibility, Digital Video Broadcast (DVB), parallel (LVDS), and serial (ASI) interfaces are also offered.

"With our new and existing product offerings, our company is ideally positioned to play a major role in the development and deployment of digital television technologies," says Tom Waschura, President of SyntheSys. "We are all excited for what the future holds for the convergence of our two major technologies – digital video test and digital communications test."



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About SyntheSys Research, Inc.

SyntheSys Research, Inc. established in 1989, manufactures advanced digital channel error analyzers for converging communications technologies including digital video, high definition video, digital satellite communication systems, fiber optic networks, and IC development. Information about SyntheSys Research can be found on the World Wide Web at www.synthesysresearch.com.

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