

FOR IMMEDIATE RELEASE

SyntheSys Research, Inc. announces the introduction of the first two members of its BERTScope™ family of advanced signal integrity analysis instruments.

February 10, 2004 — Menlo Park, California — SyntheSys Research, Inc. today introduced the BERTScope™ 7500A and 12500A Analyzers, designed for applications from 100 Mb/s to 7.5 Gb/s and 12.5 Gb/s, respectively. BERTScopes are the industry's first multi-Gb/s instruments that combine the eye diagram analysis capabilities of high-bandwidth sampling oscilloscopes together with Bit Error Ratio (BER) pattern generation and analysis.

Sample-rich eye diagrams are swiftly created using the same multi-gigabit per second sampling capability as the BER detection circuit. This provides far more complete characterization of waveform variation with long PRBS or user patterns than existing digital communications analyzers, which are limited to sampling rates below 250 ksamples/second. Also, for the first time, eye diagrams give a view of sampled data that is directly correlated to BER measurements. Now, simply adjusting the BERTScope's decision threshold to a mask violation and turning on BitAlyzer® Error Location Analysis™ enables the user to easily determine which specific bit and pattern sequence is responsible for an eye diagram mask violation, worst case jitter, or slowest rise time.

The BERTScope Analyzer with BitAlyzer Error Location Analysis™ performs seven types of advanced error analysis. Use patented Pattern Sensitivity and Error Free Interval Analysis to swiftly identify pattern-dependent and frequency-dependent errors. Proprietary high-resolution (sub-picosecond), self-calibrating delay lines (patent pending) enable accurate measurement and separation of jitter into deterministic and random components through Jitter Peak (jitter bathtub, BERT scan) Analysis, as well as accurate BER Contour Analysis (again correlated with the sample-rich eye diagram display).

New high-speed serial interfaces like CEI, XFP/XFI, 10 Gb Ethernet, and both 4 Gb and 10 Gb Fibre Channel, are creating new demands for test and detailed performance analysis. SyntheSys Research BERTScope Analyzers feature true differential outputs and inputs with variable decision threshold settings, critical for accurate differential I/O testing and analysis such as Q-Factor Analysis and BER Contour Analysis. BERTScopes reduce required capital investments by simplifying and speeding test and detailed performance analysis with built-in sinusoidal jitter generation and jitter tolerance testing together with integrated stressed eye generation and testing.

BERTScope™ to be Introduced at OFC

SyntheSys will introduce the BERTScope12500A 12.5 Gb/s Analyzer at the Optical Fiber Communication Conference at the Los Angeles Convention Center, February 22 through 27, 2004. Visit SyntheSys Research, Inc. at Booth #1713 for a demonstration.

U.S. Pricing and Availability

SyntheSys Research's BERTScope Analyzers are priced as follows: the SyntheSys Research 12500A 12.5 Gb/s analyzer starts at \$135,000; the 7500A 7.5 Gb/s analyzer starts at \$95,000. The 7500A may be upgraded for an additional charge to a 12500A for future application requirements at 10 Gb/s. First shipments are expected in May 2004. Additional information is available at www.synthesysresearch.com or call (650)-364-1853.

About SyntheSys Research, Inc.

SyntheSys Research, Inc., supplies advanced digital channel error analysis instruments to all areas of the telecomm and datacom industries. A privately held California corporation founded in 1989, the company's mission is to develop new, advanced test instruments in high-speed electronics. SyntheSys' patented BitAlyzer® analyzers study the location of errors in a data stream in addition to counting errors, providing more detailed information for engineers to discover the source of errors. For further information, call (650) 364-1853 or see the company website at www.synthesysresearch.com.

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