

FOR IMMEDIATE RELEASE

BERTScope™ Live Data Analysis to be introduced at DesignCon 2005

January 26, 2005 — MENLO PARK, California — SyntheSys Research will introduce a new Live Data signal integrity analysis option for its BERTScope™ analyzer family at DesignCon in the Santa Clara Convention Center, February 1 through February 2, 2005. You are invited to visit SyntheSys Research at the TecPreview Village Booth # 912 between 12:30 pm to 6:30 pm for a demonstration.

SyntheSys Research BERTScopes combine eye diagram analysis, jitter analysis and BERT (bit error ratio test) pattern generation and error analysis in a single instrument. Now, bridging the gap between traditional physical layer BERTs, protocol analyzers and sampling oscilloscopes, the BERTScopes are able to make measurements of jitter (ANSI standard TJ, DJ, and RJ), BER contours, and Q-factor, as well as eye diagrams, on non-repetitive data such as live traffic and system level traffic with inserted idle bits or added protocol.

Design engineers, signal integrity engineers, and test engineers implementing the latest high speed interfaces such as 4 Gb/s Fibre Channel, second and third generation Serial-ATA, Serial Attached SCSI, Fully Buffered DIMM and 6 and 11 Gb/s OIF-CEI now have the ability to measure very low probability events at the full data rate in order to swiftly and accurately assess jitter performance and analyze transmission link eye patterns in terms of BER contours to error levels of 1×10^{-12} and beyond in system level applications where the data stream under test is unknown.

BERTScope live data analysis is performed by utilizing a second high-resolution delay and decision threshold in its error detector to supply reference bit decisions for use with its physical layer test suite.

U.S. Pricing and Availability

Live Data Analysis is a \$14,500.00 option on SyntheSys Research BERTScopes (a second option, Physical Layer Analysis, is also required). Live Data Analysis is also available as an upgrade to existing BERTScopes. Available from February 1, 2005.

About BERTScope™

BERTScope™ is a trademark of SyntheSys Research, Inc., supplier of advanced digital channel error analysis instruments to all areas of the telecom, datacom, storage and computer industries. SyntheSys Research is a privately held California corporation founded in 1989 with the mission to develop new, advanced test instruments in high-speed electronics. SyntheSys' patented BitAlyzer® error location analysis™ provides detailed information including pattern-dependent error histograms enabling engineers to discover the source of errors. For further information, call (650) 364-1853 or visit our website at www.BERTScope.com.

Press Contact

Guy Foster
SyntheSys Research, Inc.
Voice: (650) 364-1853
Fax: (650) 364-5716
guy_foster@synthesysresearch.com

BERTScope™, Error Location Analysis™ and SyntheSys Research™ are trademarks and BitAlyzer® is a registered trademark of SyntheSys Research, Inc.

SyntheSys Research, Inc.	●	3475-D Edison Way	●	Phone: (650) 364-1853
info@BERTScope.com	●	Menlo Park, CA 94025	●	Fax: (650) 364-5716