

## New Capabilities for PCI Express, SATA and SAS Serial Bus Testing

### The Latest Additions to the BERTScope™ Family

**October 24, 2006 – MENLO PARK, California** – SyntheSys Research, a developer and manufacturer of high-speed signal integrity test and measurement solutions for the computer, storage, and communications industries, introduces the new BERTScope “B” family of products for testing from 0.1 to 12.5 Gb/s.

Serial buses in computer and storage applications have some unique challenges including compliance testing and clocking of test devices. Design engineers testing to the latest PCI Express, Serial-ATA, Serial Attached SCSI and Fully Buffered DIMM standards can utilize the new BERTScope S 7500B and S 12500B capabilities for accurate compliance testing.

To help designers successfully stimulate their devices under test, new features of the BERTScope “B” include

- comprehensive range of differential divided clock outputs for supplying test devices with an instrument-grade clock at the sub-rate commonly required to allow testing to be achieved,
- the ability to add stress to an externally supplied clock, to allow the test device’s own signal to be the source for testing,
- stressing of an external clock with Spread Spectrum Clock (SSC) present on it to provide realistic test signals.

New signal integrity analysis features for BERTScope “B” include

- analysis of physical layer parameters such as eye diagrams and jitter with SSC on the input signal,
- BER and eye diagram measurements down to 100 Mb/s, operable with internal or external clock,
- variable depth eye and mask measurements to allow shallow measurements like a sampling scope, and deeper measurements,
- active measurement and graphing of BERTScope CR clock recovery loop bandwidth and peaking, for precision compliance verification,
- jitter tolerance compliance template testing with margin evaluation and margin limit search,
- best in class analyzer input bandwidth in excess of 20GHz for the best accuracy and fidelity physical layer measurements such as jitter.

“Our customers have been continually challenged with testing their high-speed serial components, boards, and buses. The BERTScope “B” family offers fast and versatile compliance testing addressing the challenges of serial data buses with spread spectrum clocks,” said Dr. Lutz Henckels, Chief Executive Officer of SyntheSys Research. “We are also pleased to offer an expanded feature set for analyzing signal integrity problems, providing more insight into bit errors and saving development time.”

The flexible BERTScope S<sub>PG</sub> 12500B is available with a stress generator only. An upgrade path from any BERTScope product to the new BERTScope S 7500B and S 12500B is available for the extensive installed base of customers.

#### Availability

Delivery for the BERTScope S 7500B Stress Generator and Analyzer (7.5Gb/s), S 12500B Stress Generator and Analyzer (12.5Gb/s) and S<sub>PG</sub> 12500B Stress Generator only (12.5Gb/s) are 6 weeks ARO.

## About BERTScope™

BERTScope™ is a trademark of SyntheSys Research, Inc., a privately held California corporation. Founded in 1989, its mission is to develop advanced test instruments for identifying and locating the source of errors in high-speed digital bit streams. BERTScope pairs with BERTScope CR to offer the vision of a scope, the confidence of a BERT, and clock recovery you can count on. More information is available at [www.bertscope.com](http://www.bertscope.com).

**Media Contact:** Charlie Schaffer, Vice President of Marketing, SyntheSys Research, +1-650-364-1853.

XXXX

