

## SyntheSys Research at PCI-SIG Developer's Conference

### *Demonstrating PCIe 2.0 Test Techniques*

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**MENLO PARK, CA – May 21, 2007** – SyntheSys Research, Inc. a developer and manufacturer of high-speed signal integrity test and measurement solutions for the computer, storage, and communications industries, demonstrates new PCI-Express (PCIe) 2.0 test techniques at the 2007 PCI-SIG Developer's Conference on May 21 – 22 at the San Jose McEnery Convention Center in San Jose, California.

The BERTScope 7500B with the newly released CR<sub>J</sub> are the centerpieces of a comprehensive PCIe 2.0 Receiver Tolerance Test solution from SyntheSys Research, Inc. The solution generates the five sources of jitter – Low frequency RJ and DJ, High frequency RJ and DJ, plus ISI – meeting the standards with precise jitter sweep capabilities for PCIe 2.0 receiver tolerance and margin tests.

A novel PLL (Phase Lock Loop) Bandwidth Tester technique, compliant with the new PCIe 2.0 test requirements, will also be demonstrated. The technique uses the phase modulation method and therefore works on all types of PCIe PLL implementations. It is extremely accurate in both frequency, bandwidth, and peaking measurements and can be performed in one-tenth of a second – with a single instrument. PowerPoint slides showing the PLL Bandwidth Test technique can be found at [http://www.synthesysresearch.com/Literature/PLL\\_BW\\_setup.pdf](http://www.synthesysresearch.com/Literature/PLL_BW_setup.pdf)

The 20+ GHz bandwidth of the BERTScope 7500B allows high resolution measurements of the PCIe transmitters for both PCIe 2.0 and 3.0. While the testing of add-in card transmitters is relatively easy, tests of host transmitters for 2.0 require special techniques. An innovative method for removing reference clock jitter from the host transmitter test will be shown at the BERTScope Booth #13.

#### **About BERTScope™**

BERTScope™ is a trademark of SyntheSys Research, Inc., a privately held California corporation. Founded in 1989, its mission is to develop advanced test instruments for identifying and locating the source of errors in high-speed digital bit streams. BERTScope pairs with BERTScope CR or CR<sub>J</sub> to offer the vision of a scope, the confidence of a BERT, and clock recovery you can count on. More information is available at <http://www.synthesysresearch.com/>

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