

BERTScope™ Introduces Complete SuperSpeed USB Receiver Test Solution at the USB Compliance Workshop

MENLO PARK, CA, November 6, 2009 – SyntheSys Research, Inc., announces new capabilities on the BERTScope that enable complete SuperSpeed USB (USB 3.0) receiver testing on Hosts and Devices, as shown and used at the USB Compliance Workshop (#70) in Portland, Oregon. The new Symbol Filtering software option on the BERTScope allows for asynchronous bit error ratio (BER) testing, a key component of SuperSpeed USB receiver testing.

As is common in 8b/10b encoded serial data systems such as USB 3.0, the recovered clock frequency in the receiver may be different from that of the transmitted clock. Inserted or deleted clock compensation symbols are used to adjust for this difference, and can cause problems for typical BERT systems, with the exception of BERTScope. The BERTScope can now filter out clock compensation symbols and maintain synchronization for successful asynchronous BER and Jitter Tolerance testing, in addition to its existing ability to perform BER testing in a synchronous manner.

Furthermore, a new capability on the BERTScope Clock Recovery instruments assists in USB 3.0 transmitter testing. The USB 3.0 golden PLL Jitter Transfer Function (JTF) Bandwidth of 4.9 MHz for transmitter testing is made possible by the new Extended Loop Bandwidth option on the BERTScope Clock Recovery products. The higher 24 MHz loop bandwidth maximum is the highest in available hardware clock recovery instruments and accommodates the USB 3.0 JTF Bandwidth as well as those of other serial bus standards such as 3G and 6G SATA.

The BERTScope makes USB 3.0 testing easy with an integrated solution providing stress generation and innovative BER measurement in one instrument. This enables a simple setup so that usable measurement results can be obtained quickly. With troubleshooting tools that go beyond compliance, the BERTScope is an obvious choice for USB 3.0 testing and characterization.

Availability

The Symbol Filtering and Extended Loop Bandwidth options are available 8 weeks ARO.

About SyntheSys Research, Inc.

Founded in 1989, SyntheSys Research, Inc., innovator of the award-winning BERTScope™, is a privately held corporation located in Menlo Park, California. SyntheSys develops and manufactures high-speed signal integrity test and measurement instrumentation for the computer, storage and communications industries. Learn more at www.bertscope.com.

Media Contact: guy_foster@bertscope.com, 707-636-2515
